

# Call for Participation

## IEEE 2023 Asian Solid-State Circuit Conference (A-SSCC 2023)

### 1. Date

November 5-8, 2023

### 2. Location

Star Bay Pullman Hotel, Haikou, Hainan island, China

### 3. About the conference

The IEEE A-SSCC 2023 (Asian Solid-State Circuits Conference) is an international forum for presenting the most updated and advanced chips and circuit designs in solid-state and semiconductor fields. The conference is supported by the IEEE Solid-State Circuits Society and will be held in Asia. The Conference offers a unique opportunity for scientific researchers and engineers working at the cutting edge of IC design and application to maintain technical currency, and to network with leading experts.

### 4. Conference Schedule

November 5, 2023 (Sunday)	
09:00-10:20(80')	<b>Tutorial 1</b> <b>Hybrid DC-DC Converters</b> <i>Speaker: Yan Lu, University of Macau</i> (Tianjin Hall Grand Ballroom A)
10:20-10:40(20')	Break
10:40-12:00(80')	<b>Tutorial 2</b> <b>Circuit Design for Scalable Quantum Computing</b> <i>Speaker: Jae-Yoon Sim, POSTECH</i> (Tianjin Hall Grand Ballroom A)
12:00-13:20(80')	Lunch

13:20-14:40(80')	<b>Tutorial 3</b> <b>Digital Equalization for Multilevel Signaling in High-speed SerDes</b> <i>Speaker: Masum Hossain, Carleton University</i> (Tianjin Hall Grand Ballroom A)		
14:40-15:00(20')	Break		
15:00-16:20(80')	<b>Tutorial 4</b> <b>Reconfigurable ML Processor: Fundamental Concepts, Application, and Future Trends</b> <i>Speaker: Shouyi Yin, Tsinghua University</i> (Tianjin Hall Grand Ballroom A)		
16:40-18:20(100')	<b>Learn to Establish and Achieve Professionalism (LEAP)</b> (Tianjin Hall Grand Ballroom A)	<b>Start-up!</b> (Tianjin Hall Grand Ballroom B)	<b>IT Vision in Asia</b> (Coral Hall)
18:20-19:20(60')	<b>TPC Meeting</b> (Tianjin Hall Grand Ballroom B)		
19:30-21:00(90')	<b>Welcome Reception</b> (Riverside Lawn)		
November 6, 2023 (Monday)			
08:40-9:00(20')	<b>Opening</b> (Tianjin Hall Grand Ballroom)		
09:00-09:45(45')	<b>Plenary Speech 1</b> <b>Semiconductor Chip Design in a Legoland</b> <i>Speaker :Chih-Ming Hung, AVP of Technology</i> (Tianjin Hall Grand Ballroom)		
09:45-10:30(45')	<b>Plenary Speech 2</b> <b>Envisioning 6G Mobile New World</b> <i>Speaker : Bongtae Kim, ETRI Fellow</i> (Tianjin Hall Grand Ballroom)		
10:30-10:50(20')	Break		
10:50-12:30(100')	<b>Session 1</b> <b>Advanced Designs for Industrial Applications</b> (Tianjin Hall Grand Ballroom A)	<b>Rising Star Express (RiSE)</b> (Tianjin Hall Grand Ballroom B)	<b>WiC &amp; YP Panel</b> (Coral Hall)
12:30-14:00(90')	Lunch		
14:00-15:40(100')	<b>Session 2</b> <b>Analog Techniques</b> (Tianjin Hall Grand Ballroom A)	<b>Session 3</b> <b>Energy-Efficient SoCs: From Tiny Accelerator to Huge GPU</b>	<b>Session 4</b> <b>Bio and Neuromorphic System</b> (Coral Hall)

		(Tianjin Hall Grand Ballroom B)	
15:40-16:00(20')	Break		
16:00-17:40(100')	Session 5 Computing and Analog Circuit for Next Generation Memories (Tianjin Hall Grand Ballroom A)	Session 6 THz Transceivers (Tianjin Hall Grand Ballroom B)	Session 7 Emerging Sensors and Displays (Coral Hall)
17:40-18:00(20')	Break		
18:00-19:30(100')	Social Event (Outdoor area outside the corridor of the conference center)		
November 7, 2023 (Tuesday)			
09:00-09:45(45')	Plenary Speech 3 Architecture Challenges for Heterogeneous Processors in Embedded SoCs <i>Speaker: Masayuki Ito, Director, NSITEXE Inc.</i> (Tianjin Hall Grand Ballroom)		
09:45-10:30(45')	Plenary Speech 4 Listen: ESD Protection is About Circuit Design <i>Speaker: Albert Wang, University of California, Riverside</i> (Tianjin Hall Grand Ballroom)		
10:30-10:50(20')	Break		
10:50-12:30(100')	Session 8 Voltage Regulators (Tianjin Hall Grand Ballroom A)	Session 9 Energy-Efficient Application-Specific Processors (Tianjin Hall Grand Ballroom B)	Session 10 Advanced Wireline Transceiver Techniques (Coral Hall)
12:30-14:00(90')	Lunch		
14:00-15:40(100')	Session 11 Innovative Nyquist ADCs (Tianjin Hall Grand Ballroom A)	Session 12 Phased Arrays (Tianjin Hall Grand Ballroom B)	Session 13 AI on FPGA (Coral Hall)
15:40-16:00(20')	Break		
16:00-17:40(100')	Panel Discussion Heterogeneous Integration: Does it call the end of the foundry-fabless business model? (Tianjin Hall Grand Ballroom A)	Industry Forum (Tianjin Hall Grand Ballroom B)	
17:40-18:00(20')	Break		
18:00-20:00(120')	Banquet		

	(Tianjin Hall Grand Ballroom)		
November 8, 2023 (Wednesday)			
09:00-10:40(100')	Session 14 High Precision Oversampling ADCs (Tianjin Hall Grand Ballroom A)	Session 15 High-Performance Transceivers (Tianjin Hall Grand Ballroom B)	Session 16 Intelligent Memory and Logic Circuit Techniques (Coral Hall)
10:40-11:00(20')	Break		
11:00-12:40(100')	Convergence Workshop (Tianjin Hall Grand Ballroom A)	Highlights from ESSCIRC (Tianjin Hall Grand Ballroom B)	Highlights from CICC  (Coral Hall)
12:40-14:00(80')	Lunch		
14:00-15:40(100')	Session 17 Power Management Techniques (Tianjin Hall Grand Ballroom A)	Session 18 High-Performance PA and VCO (Tianjin Hall Grand Ballroom B)	Session 19 Emerging LiDAR Sensor and Security Chips (Coral Hall)
15:40-16:00(20')	Break		
16:00-17:40(100')	Session 20 Wireless Power Transfer (Tianjin Hall Grand Ballroom A)	Session 21 Application-oriented ADCs (Tianjin Hall Grand Ballroom B)	Session 22 High-performance Clock Generation (Coral Hall)